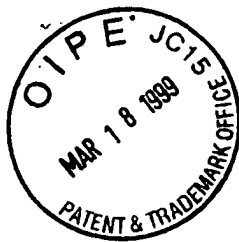


MSLIN-98-005



March 8 1999

CAU 2785 #2
7-27-99
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To: Commissioner of Patents and Trademarks
Washington, D.C. 20231

Fr: George O. Saile, Reg. No. 19,572
20 McIntosh Drive
Poughkeepsie, N.Y. 12603

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Subject:

Serial No. 09/246,303 02/08/99

M.S. Lin

A MULTIPLE SELECTABLE FUNCTION
INTEGRATED CIRCUIT MODULE

Grp. Art Unit: 2835

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INFORMATION DISCLOSURE STATEMENT

TECHNOLOGY CENTER 2800


Enclosed is Form PTO-1449, Information Disclosure Citation
In An Application.

The following Patents and/or Publications are submitted to
comply with the duty of disclosure under CFR 1.97-1.99 and
37 CFR 1.56. Copies of each document is included herewith.

U.S. Patent 5,737,767 to Agrawal et al., "System for
Reconfiguring the Width of an X-Y Ram", teaches a random access
memory (RAM) array with a reconfigurable bit width or word
width.

COMM. & INT. SEC.
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U.S. Patent 5,360,992 to Lowrey et al., "Two Piece Assembly for the Selection of Pinouts and Bond Options on a Semiconductor Device", discloses a semiconductor package which allows pinouts and bond options to be customized after encasement of a semiconductor die. The semiconductor package has two assemblies in a first embodiment and an optional third assembly in a second embodiment.

Sincerely,

Stephen B. Ackerman,
Reg. No. 37661

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